



COTC PATENT

5298-19203/CSC01001-C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor/Applicant:  
Chen

Serial No. 10/656,306  
Filed: September 4, 2003

Patent No. 6,826,106  
Issued: November 30, 2004

For: ASYNCHRONOUS HIDDEN REFRESH  
OF SEMICONDUCTOR MEMORY

Atty. Docket No. 5298-19203

Group Art Unit: 2824

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313, on the date indicated below:

6/9/05  
Date

Kevin L. Daffer

TRANSMITTAL LETTER

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

Dear Sir:

Certificate  
JUN 17 2005  
of Correction

Transmitted herewith for filing in the captioned case are the following:

- (1) Form PTO-1050, **submitted in duplicate**. Errors which occur at important points in the captioned patent or which may otherwise affect the understanding or interpretation of the patent are thereon corrected.
- (2) A return postcard to acknowledge receipt of these materials. Please stamp and return this postcard to the undersigned.

All of the errors shown in PTO-1050 are due to Patent Office oversights. A Certificate of Correction is requested under 35 U.S.C. § 254. Applicants believe that no fees are required, however, should any fees be required, please deduct them from deposit account number 50-3268/5298-19203.

Respectfully submitted,

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Date: 6/9/05

JUN 21 2005

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO. 6,826,106

DATED: November 30, 2004

INVENTOR(S): Chen

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 22

col. 14, line 10: After the phrase "memory coupled to" please delete "recycle" and substitute therefor --receive--.

## MAILING ADDRESS OF SENDER:

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PATENT NO. 6,826,106

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**Certificate of Correction (PTO Form 1050)**

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